IN THE UNITED STATES PATENT AND TRADEMARK OFFICE licant(s): Uneme, Masakatsu Serial No.: 10/657,464 Group Art Unit: 2185 Filed: September 8, 2003 Examiner: Kim, Hong Chong Title: Data Processing Apparatus that Shares a Single Semiconductor Memory Circuit Among Multiple Data Processing Circuits Attorney Docket No.: N26532602E TRANSMITTAL COVER SHEET (37 C.F.R. §1.8) Mail Stop R.C.E. Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313 The following is transmitted herewith: Fee Transmittal Form Certified Copy of Priority Documents $[\]$ Fee Attached Response to Missing Parts/Incomplete [X][] Extension of Time Request **Assignment Papers** [X][] Information Disclosure Statement Power of Attorney/Revocation/Change Status Request Letter of Correspondence Address [X]Request for Continued Examination **Conditional Petition for Extension of Time** It is believed that the indicated items are timely filed. However, in the event an extension of time is required, please consider this a petition for extension of time under 37 C.F.R. §1.136(a). Please charge any fees that may be required to Deposit Account No. 50-2087. Respectfully Submitted, June 22, 2006 Darryl G. Walker, Reg. No. 43,232 Date 37 C.F.R. §1.8 I hereby certify that this correspondence is being [] transmitted via facsimile to the United States Patent and Trademark Office to fax number: Date of Transmittal: [X] deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313. Date of Deposit: _June 22, 2006_ Typed/Printed Name: Darryl G. Walker Signature: